JC062 U.S. PTO
09/639613
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	Subclass	
Class		ISSUE CLASSIFICATION

C

U.S. **UTILITY** Patent Application

O.I.P.E.

SCANNED   Q.A.

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/639613		705 716	4	2161 2825	TAT

APPLICANTS

John McBride
Jan Kok

TITLE

Electrical rules checker system and method using tri-state logic
for electrical rule checks

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PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)				
CLASS	**SUBCLASS**	**CLASS**	**SUBCLASS (ONE SUBCLASS PER BLOCK)**				
INTERNATIONAL CLASSIFICATION							

— Continued on Issue Slip Inside File Jacket

☐ **TERMINAL** ☐ **DISCLAIMER**	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.

☐ The term of this patent
subsequent to _____ (date)
has been disclaimed.

(Assistant Examiner)   (Date)

NOTICE OF ALLOWANCE MAILED

☐ The term of this patent shall
not extend beyond the expiration date
of U.S Patent. No. _____

_____

_____

(Primary Examiner)   (Date)

ISSUE FEE

Amount Due	Date Paid

☐ The terminal ____ months of
this patent have been disclaimed.

(Legal Instruments Examiner)   (Date)

ISSUE BATCH NUMBER

Form PTO-436A
(Rev. 6/99)

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